

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Shunpei YAMAZAKI et al.

Serial No. 10/815,701

Filed: April 2, 2004

For: SEMICONDUCTOR DEVICE

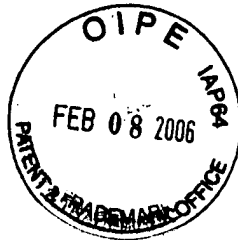
EQUIPPED WITH

SEMICONDUCTOR CIRCUITS

COMPOSED OF SEMICONDUCTOR

ELEMENTS AND PROCESS FOR

PRODUCTION THEREOF



) Group Art Unit: 2815

) Examiner: Edgardo Ortiz

) CERTIFICATE OF MAILING

) I hereby certify that this correspondence is
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) 2006.

) Adrian M. Stamps

AFTER FINAL AMENDMENT

Honorable Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Official Action dated October 4, 2005, please consider the following amendments and remarks in connection with the above-identified application.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 11 of this paper.